

# Model 197 High Stability OCXO

#### **Features**

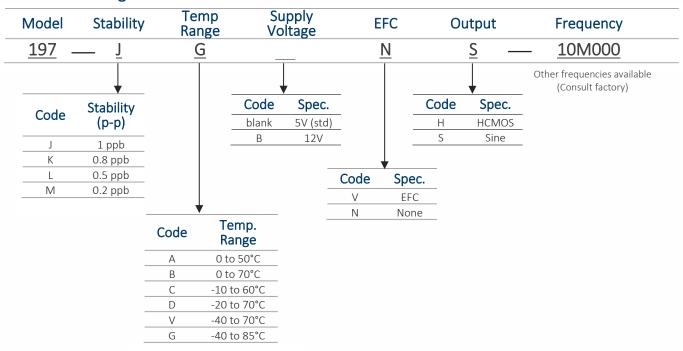
- 1 μsec typical for 8 hour holdover
- -40°C to 85°C Operating Temperature Range
- Sinewave or HCMOS Output
- Industry Standard 36 x 27 mm package



#### Description

CTS Model 197 is a high stability double oven OCXO developed to meet stringent Long-Term Evolution (LTE) performance requirements, offering 1.0 ppb (p-p) temperature stability over the industrial temperature range. It provides excellent 1  $\mu$ sec typical holdover performance with  $\pm 10^{\circ}$ C temperature variation. Other applications ideally suited for this OCXO include high performance instrumentation, communications systems, and rubidium replacement.

Table 1: Ordering Information



Note: Not all stabilities are available for all input voltages. Please consult factory.

Part Number Example: 197-JGNS-10M000



# **Electrical Specifications**

Parameter	Conditions & Remarks	Min	Typical	Max	Unit
Operating Conditions					
Supply Voltage	V <sub>CC</sub>	4.75	5	5.25	Vdc
See Table 1 options)	VCC	11.4	12	12.6	vac
Turn-On Power	Nom V <sub>CC</sub>	-	_	6.0	W
Steady State Power	Nom V <sub>CC</sub> , Ta = 25°C	-	1.5	2.0	W
Operating Temperature Range	e Ta	-40	-	+85	°C
(See Table 1 options)					
Frequency Stability					
Frequency	F <sub>NOM</sub> , Other frequencies available (consult factory)		10.000		MHz
Calibration	ΔF/F <sub>NOM</sub> ;	-	-	±100	ppb
Temperature Stability	$\Delta$ F/ F <sub>NOM</sub> over -40 to +85°C	-	0.5	1.0	ppb p-p
(See Table 1 options)	Hysteresis	-	-	0.2	ppb
Frequency vs. Voltage	V <sub>CC</sub> ±5%	-	-	±0.3	ppb
Aging	Per day	-	-	±0.2	ppb
<b>Aging</b> (10MHz)	Per year	-	-	±20	ppb
(±0101П <i>Z)</i>	10 years	-	-	±180	ppb
8 Hour Holdover	With ±10°C temperature	_	<1	1.5	μsec
	variation – see Figure 1			1.5	μισου
Warm up time	Within 20 ppb, ref to 1 hour	-	-	5	minutes
Voltage Range	ntrol (EFC) (See Table 1 options Vc, Control voltage range	0	-	4	V
Pulling Range	Positive, monotonic	±0.3	-	_	ppm
Linearity		-	-	10	%
Output Parameters					
Sinewave (See Table 1 options)	50 Ω load	+5	+7	+9	dBm
Load		45	50	55	Ω
Harmonics				-30	dBc
	Load		15 pf		
CMOS	5V option	2.7	-	-	
(See Table 1 options)	V <sub>OH</sub> 12V option	3.5	-	-	Vdc
·	Vol	-	-	0.4	
Rise / Fall times	$T_R / T_F$	-	-	5	ns
Duty Cycle		45	50	55	%
Spurious				-70	dBc
	Offset = 1Hz	-	-	-90	
	10Hz	-	-	-120	
Phase Noise	100Hz	-	-	-140	dBc/Hz
(10MHz)	1KHz	-	-	-150	GDC/11Z
	10KHz	-	-	-155 155	
	100KHz	-	-	-155	



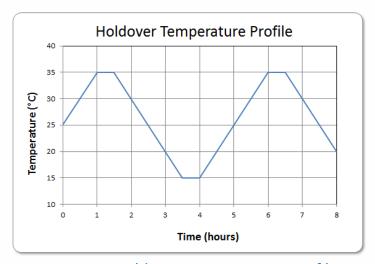


Figure 1. Holdover Temperature Profile

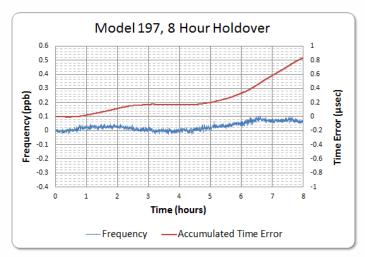


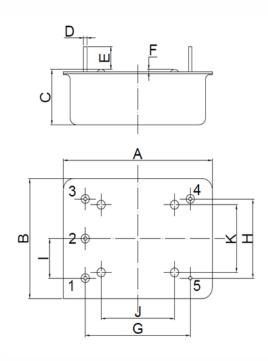
Figure 2. Typical holdover performance

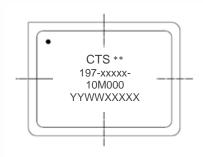
### Mechanical and Environmental

Storage Temperature	-55 to +105°C	
Drop Test	The test shall be carried out as the provisions of the IEC60028-2-32 test Ed.	
	10 cm height, 3 times on hard board with thickness of 3 cm.	
Bump Test	Device are bumped in three mutually perpendicular axes at peak acceleration of 400m/s <sup>2</sup> ,	
	each 4000 ±10 times, 6ms pulse duration time.	
Vibration	The test shall be carried out as the provisions of the IEC60068-2-06 test Fc.	
	Frequency range: 10Hz-500Hz	
	Acceleration: 10g	
	Displacement: 0.75mm@10Hz	
	Duration: 30 minutes per axis (3 axes)	
Mechanical Shock	The test shall be carried out as the provisions of the IEC60068-2-27 Ed.	
	100g, 11mS duration, 1/2 sine wave, 3 shocks each direction along 3 mutually	
	perpendicular planes.	
Thermal Shock	0.5h@-40°C, 0.5h@+85°C. Note: the changing time < 30 seconds, cycling for 100 times	



## **Mechanical Specifications**





#### Pin Assignments

Pin	Function	
1	Vc / NC*	
2	DNC**	
3	Vcc	
4	RF Output	
5	GND/Case	

<sup>\*</sup> NC when EFC option "N" is selected.

#### Dimensions (mm)

Symbol	Min	Max
А		36.4
В		27.4
С		13.0
D	0.7	0.9
Е	4.5	5.9
F	0.4	0.7
G	25.2	25.6
Н	17.6	18.0
I	8.8	9.0
J	17.75 nominal	
K	15.21 nominal	

#### Marking

**	Mfg Site Code
YYWWXXXXX	DC/Serial Number

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.

<sup>\*\*</sup> DNC - Do not connect to this pin.