





UPB048A

Band 48 (3.55-3.70GHz) UPB Series TDD BPF

Features

- Meets FCC CBRS emission requirements for 0.25W@Ant systems
- Low Loss with High Rejection
- Universal footprint across family for all TDD bands

Applications

- Wireless Infrastructure applications
- High-performance carrier-grade TDD Pico-cells.



Description

Surface mount ceramic bandpass filter supports a universal footprint across all TDD frequency bands enabling the use of a common system PCB. Superior rejection, insertion loss, reliability, as well as both peak and average power handling compared to other bandpass filter technologies.

Electrical Specifications

Parameter	Frequency (MHz)	Typical at 25°C	Spec. at 25°C	Spec. over -40°C to +85°C
Nominal Impedance	-	50 ohms	-	-
Average Input Power	-	-	-	2.0 Watt max
Peak Input Power	-	-	-	20 Watt max
Input-Output Response				
Passband Insertion Loss (20 MHz avg)	3550-3700	1.9 dB	2.1 dB max	2.1 dB max
Passband Insertion Loss (10 MHz avg)	3550-3700	2.1 dB	2.3 dB max	2.5 dB max
Passband Ripple (over 20 MHz)	3550-3700	1.0 dB	1.3 dB max	1.6 dB max
Passband Return Loss	3550-3700	11 dB	10 dB min	10 dB min
Attenuation:	1-2690	30 dB	27 dB min	27 dB min
	2690-3300	11 dB	9 dB min	9 dB min
	3300-3530	7 dB	6 dB min	5 dB min
	3720-3800	7 dB	6 dB min	5 dB min
	3800-4200	11 dB	9 dB min	9 dB min
	4200-4400	25 dB	20 dB min	20 dB min
	4400-5925	35 dB	27 dB min	27 dB min
	7100-7400	20 dB	15 dB min	15 dB min

Note: CTS tests each unit to the critical specifications above. Subsequent audits may deviate due to repeatability among different test systems which shall not exceed these allowances. Specification Allowance Insertion Loss 0.1 dB Return Loss 1.0 dB Attenuation 1.0 dB

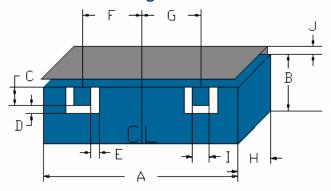
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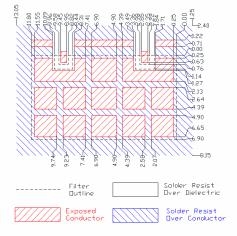
PRELIMINARY – UPB048A

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Mechanical Drawing



PCB Layout



Packaging and Marking

Dimensi	on Unit	s Spec.	Product	Marking			
Reel Diam	eter mm	330		CTS			
Reel Wei	ght kg	???		148			
Reel Quar	ntity ea.	500		VW			
Customer Feed Direction $ ightarrow$							
PoMM/(Inches)		MM/(Inche	Wo MM/(Inches)	Bo MM/(Incl			
Wo	A_{o}	Bo	Ko	Po			
0.945 in	0.268 in	0.366 in	0.132 in	0.472 in			
24.0 mm	6.80 mm	9.30 mm	3.35 mm	12.0 mm			

Nominal Tolerance Dim. (mm) (±mm or Max) Α 8.97 0.20 В 5.30 max C 0.76 0.13 0.38 D 0.13 Ε 0.38 0.13 F 3.30 0.13 G 3.30 0.13 Н 3.10 max 1 0.76 0.13 J 0.95 0.20

IMPORTANT: Please assure >=30mils (0.75mm) thickness of dielectric beneath the I/O Pads <u>and</u> the surrounding clearance zone down to the required ground plane.

Please assure sufficient ground vias between the top metal ground plane and the primary ground plane.

Recommended solder: 4-6 mils of SAC305 with reflow incl. 120s of soak at 217°C, and up to 30 sec peak at 241°C.

NOTE: The width of 9.50mm is necessary to support frequencies as low as 1885MHz for Band 39. If only higher frequency TDD bands are supported, then a smaller space can be allocated on the layout.

Electrical Response

