





UPB400A - PRELIMINARY

3800-4200MHz UPB Series TDD Bandpass Filter

Features

- Low Loss with High Rejection
- Low ripple
- Universal footprint across family for all TDD bands

Applications

- Wireless Infrastructure applications
- High-performance carrier-grade single-band TDD Pico-cell basestations for 0.25-0.5W at the antenna port.

Part Dimensions: $10.2 \times 5.4 \times 4.0 \text{ mm} \cdot < 1 \text{ g}$ Materials: Ag plated ceramic block with tin plated brass shield

Description

Surface mount ceramic bandpass filter supports a universal footprint across all TDD frequency bands enabling the use of a common system PCB. Superior rejection, insertion loss, reliability, as well as both peak and average power handling compared other bandpass filter technologies.

Electrical Specifications

Parameter	Frequency (MHz)	Typical at 25℃	Spec. at 25°C	Spec. over -40°C to +85°C
Nominal Impedance	-	50 ohms	-	-
Average Input Power	-	-	-	2.0 Watt max
Peak Input Power	-	-	-	20 Watt max
Input-Output Response				
Passband Insertion Loss (20 MHz avg)	3800-4200	0.9 dB	1.1 dB max	1.2 dB max
Passband Ripple	3800-4200	0.5 dB	0.7 dB max	0.8 dB max
Passband Return Loss	3800-4200	14 dB	12 dB min	12 dB min
Attenuation:	1-3400		40 dB min	40 dB min
	3401-3600		15 dB min	15 dB min
	4400-5149		15 dB min	15 dB min
	5150-5950		40 dB min	40 dB min
	5951-7125		30 dB min	30 dB min
	7126-8400		20 dB min	20 dB min

Note: CTS tests each unit to the critical specifications above. Subsequent audits may deviate due to repeatability among different test systems which shall not exceed these allowances. Specification Allowance Insertion Loss 0.1 dB Return Loss 1.0 dB Attenuation 1.0 dB

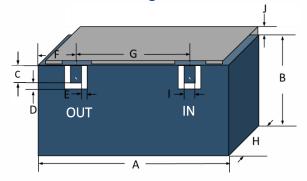
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Mechanical Drawing



PCB Layout Solder Resist Over Dielectric

Dutline

Nominal (mm)	Tolerance (±mm or Max)		
10.20	0.13		
4.00	0.30		
0.76	0.13		
0.38	0.13		
0.38	0.13		
1.80	0.13		
6.60	0.13		
3.80	0.30		
0.76	0.13		
0.90	0.30		
	(mm) 10.20 4.00 0.76 0.38 0.38 1.80 6.60 3.80 0.76		

IMPORTANT: Please assure >=30mils (0.75mm) thickness of dielectric beneath the I/O Pads and the surrounding clearance zone down to the ground plane.

Please assure sufficient ground vias between the top metal ground plane and the primary ground plane.

Recommended solder: 4-6 mils of SAC305 with reflow including 120s of soak at 217°C, and up to 30 sec peak at 241°C.

Packaging and Marking

Solder Resist Over Conductor

Dimens	ion Uni	ts	Spec.	Produ	ıct Marking		
Reel Diam	eter mn	1	330		CTS		
Reel Wei	ght kg		5.5		400		
Reel Quar			500		YWW		
Customer Feed Direction $ ightarrow ightarrow ightarrow$							
•	• • •		•	Wo MM/(inches)	Bo MM/(Inel		
Po MM/(Inches) Ao MM/(Inches)							
Wo	Ao		Bo	Ko	Po		
0.945 in	0.319 in	0	.406 in	0.165 i	n 0.472 in		
24.0 mm	8.10 mm	10	.30 mm	4.2 mn	n 12.0 mm		

