

Features

- Low Loss
- Low Ripple
- High Rejection

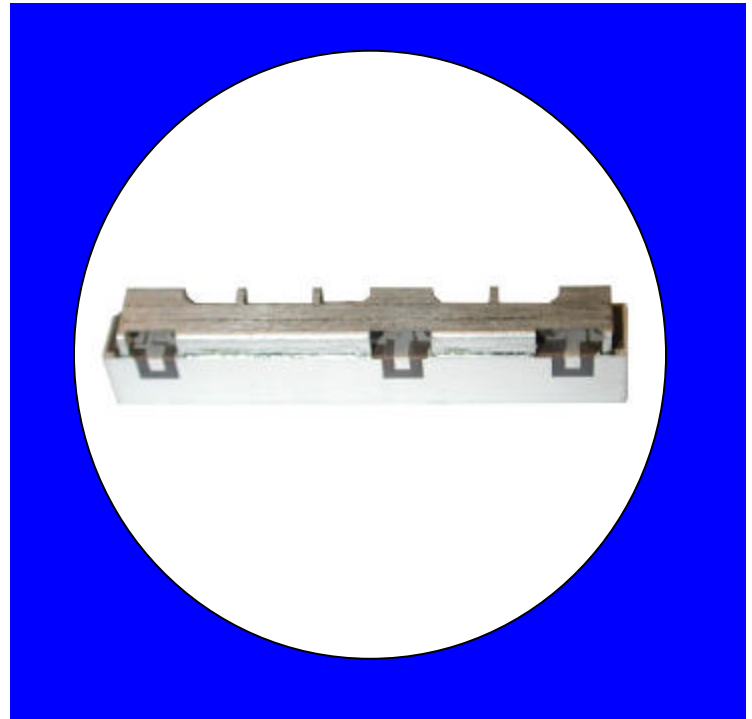
Description

Surface mount, silver (Ag) coated ceramic filter for use in broadband applications.

Weight: 7.8 grams typical.

Material: Filter is composed of a ceramic block plated with Ag.

Filter complies with RoHS standards.



Electrical Specifications

Parameter	Frequency MHz	Typical @ 25°C	Specification @ 25°C	Spec over -40°C to +70°C
Low Band Response (S12)				
Passband Iloss	3425 - 3450	-2.00	-2.70	-3.00
Passband Ripple	3425 - 3450	0.20	0.40	0.50
Passband Return Loss @ Port 1	3425 - 3450	-16.00	-14.00	-14.00
Passband Return Loss @ Port 2	3425 - 3450	-16.00	-14.00	-14.00
Attenuation	0.1 - 2475	-65.00	-35.00	-35.00
	2770 - 2971	-53.00	-45.00	-45.00
	3097 - 3211	-51.00	-40.00	-40.00
	3525 - 3550	-60.00	-50.00	-50.00
	3975 - 4675	-38.00	-35.00	-35.00
	4675 - 4975	-30.00	-25.00	-25.00
High Band Response (S31)				
Passband Iloss	3525 - 3550	-2.20	-2.70	-3.00
Passband Ripple	3525 - 3550	0.20	0.40	0.50
Passband Return Loss @ Port 1	3525 - 3550	-16.00	-14.00	-14.00
Passband Return Loss @ Port 3	3525 - 3550	-16.00	-14.00	-14.00
Attenuation	0.1 - 2475	-50.00	-35.00	-35.00
	2824 - 3067	-43.00	-35.00	-35.00
	3403 - 3430	-50.00	-30.00	-30.00
	3425 - 3450	-53.00	-50.00	-50.00
	3975 - 4400	-37.00	-35.00	-35.00
	4400 - 4975	-24.00	-20.00	-20.00
Isolation (S23)				
Rejection @ Low Band	3425 - 3450	-55.00	-50.00	-50.00
Rejection @ High Band	3525 - 3550	-55.00	-50.00	-50.00
Power into any port		2 Watt max		

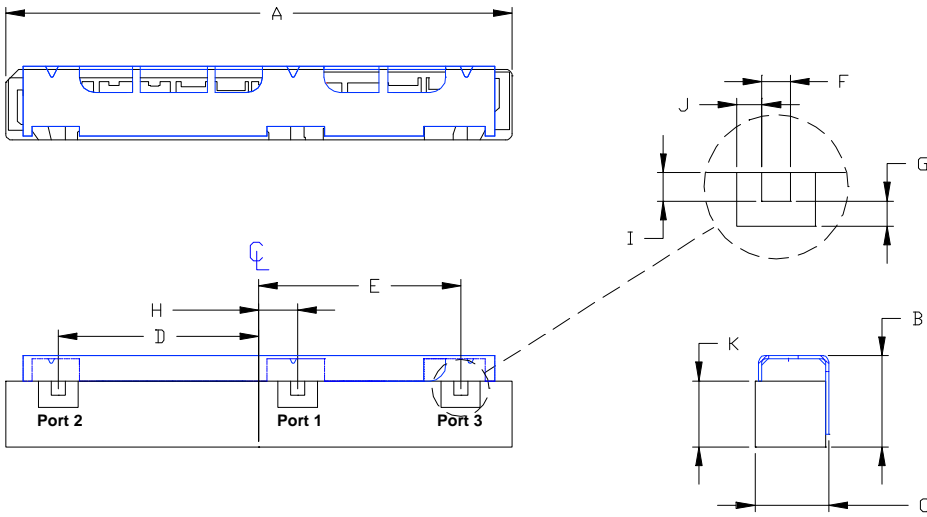
Note: Supplier shall test each filter to the critical electrical specifications of the above table. Any subsequent audits may deviate from in value due to measurement repeatability among different test systems. Such deviations shall not exceed the following limits:

Specification Allowance	
Insertion Loss	0.1 dB
Return Loss	1.0 dB
Stopbands	1.0 dB

*This product is covered by one or more of the following U.S. and foreign patents including: US 4,692,726;US 4,742,562; US 4,800,348;US 4,829,274;US 5,146,193;EP 0573597;DE 0573597;FR 0573597;JP 508149/92;KR 142171;US 5,162,760;US 5,218,329;US 5,250,916;US 5,327,109;US 5,488,335;CA 2114029;FR 9306297;GB 2273393;JP 3205337;KR 115113;CN 93106228.4;US 5,512,866;EP 0706719;DE 0706719;FR 0706719;GB 0706719;CN 95190359.4;US 5,602,518;US 5,721,520;US 5,745,018;EP 0910875;DE 0910875;DK 0910875;FR 0910875;GB 0910875;IE 0910875;JP 505182/98;KR 10-323013;US 5,994,978;US 6,462,629;CN 00810420.4;US 6,559,735;US 6,650,202;US 6,834,429. Other US and foreign patents pending.

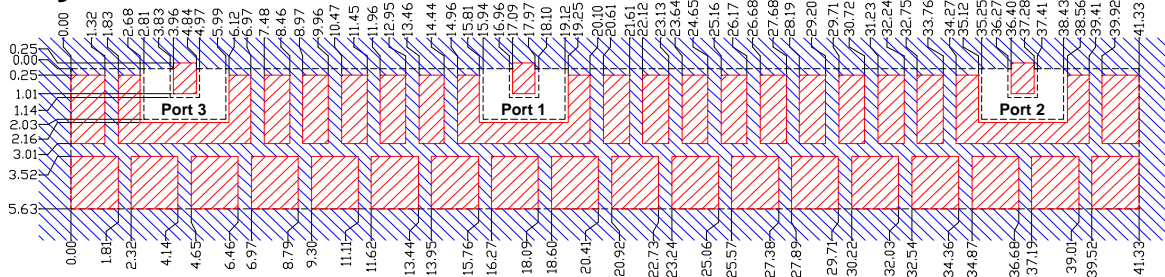
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Mechanical Drawing



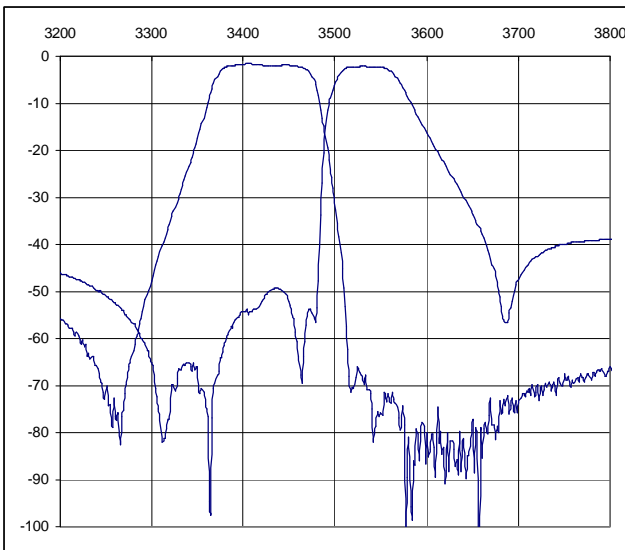
Dim	Nominal (mm)	Tolerance (mm) +/- or max
A	41.33	Max.
B	7.8	Max.
C	6.3	Max.
D	16.17	0.13
E	16.27	0.13
F	1.14	0.13
G	1.02	0.13
H	3.14	0.13
I	1.14	0.13
J	1.02	0.13
K	5.63	Max.

PCB Layout

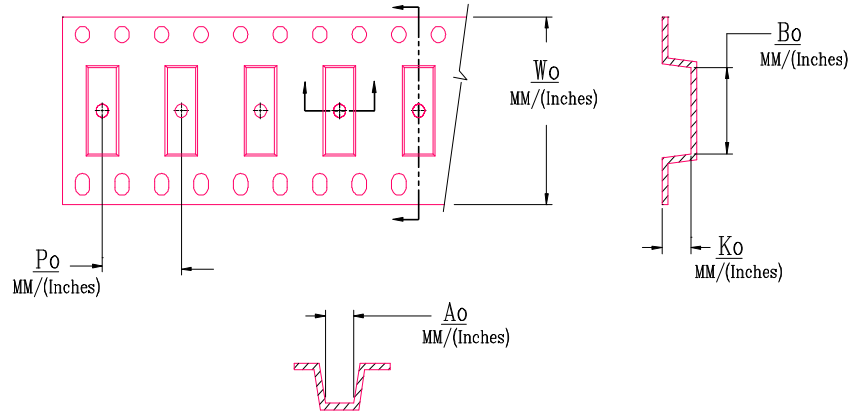


- Filter Outline
- Exposed Conductor
- Solder Resist Over Conductor
- Solder Resist Over Dielectric

Electrical response



Packaging and Marking



MODEL NO.	Wo	Ao	Bo	Ko	Po
CER0290A	56.0/(2.205)	8.10/(0.319)	41.65/(1.640)	6.5/(0.256)	16.0/(0.630)