

### Features

- High Power
- Low Insertion Loss
- High Attenuation

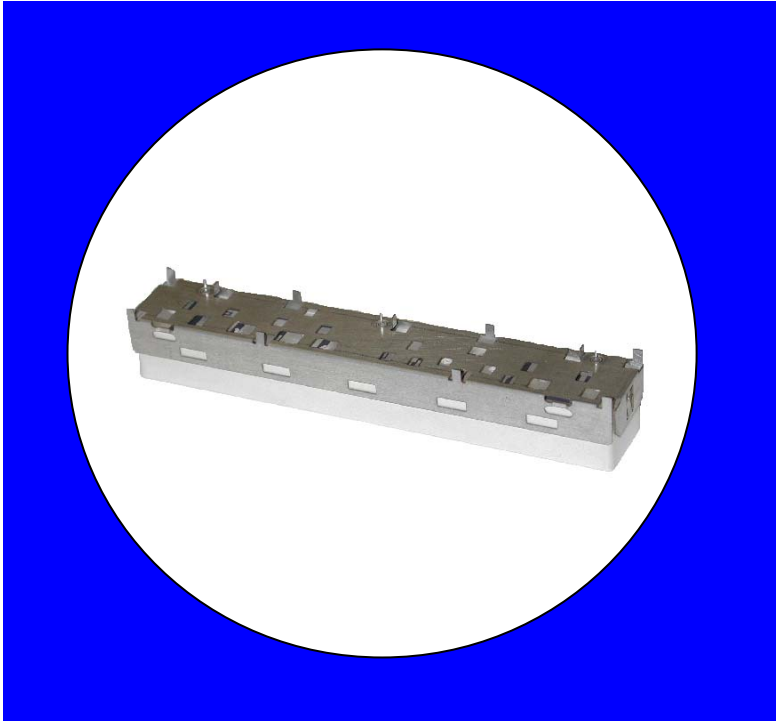
### Description

Surface mount, silver (Ag) coated ceramic duplexer for use in AMPS applications.

Weight: 0.1982 kg typical

Material: Filter is composed of a ceramic block plated with Ag and a Bracket made of nickel silver plated steel.

Filter complies with RoHS standards.



### Electrical Specifications

Parameter	Frequency MHz	Typical @ 25°C	Specification @ 25°C	Spec over -40°C to +85°C
<b>Low Band Response (S21)</b>				
Passband Iloss	824 - 849	-1.00	-1.30	-1.60
Passband Ripple	824 - 849	0.70	1.00	1.20
Passband Return Loss @ Ant	824 - 849	-13.0	-11.0	-11.0
Passband Return Loss @ Port 3	824 - 849	-13.0	-11.0	-11.0
Attenuation	869 - 894	-53.0	-45.0	-44.0
<b>Low Band Response (S13)</b>				
Passband Iloss	869 - 894	-1.10	-1.40	-1.70
Passband Ripple	869 - 894	0.70	1.00	1.20
Passband Return Loss @ Ant	869 - 894	-12.0	-10.0	-10.0
Passband Return Loss @ Port 2	869 - 894	-12.0	-10.0	-10.0
Attenuation	824 - 849	-55.0	-45.0	-44.0
<b>Isolation (S23)</b>				
Rejection @ Low Band	824 - 849	-53.0	-46.0	-45.0
Rejection @ High Band	869 - 894	-55.0	-46.0	-45.0
Average Power		20 watts		
Peak Power		200 Watts		

Note: Supplier shall test each filter to the critical electrical specifications of the above table. Any subsequent audits may deviate from in value due to measurement repeatability among different test systems. Power test will be completed with 50 watts average power in 5 MHz steps across the band. 12 steps total with a 100 millisecond pulse at each frequency point and a 200 watt peak, 1% duty factor with a 9 microsecond pulse.

Such deviations shall not exceed the following limits:

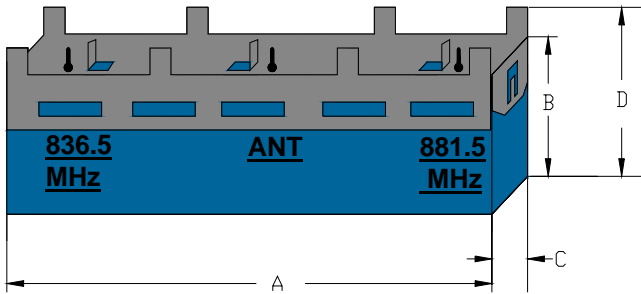
Specification Allowance	
Insertion Loss	0.1 dB
Return Loss	1.0 dB
Stop bands	1.0 d

\*This product is covered by one or more of the following U.S. and foreign patents including: US 4,692,726;US 4,742,562; US 4,800,348;US 4,829,274;US 5,146,193;EP 0573597;DE 0573597;FR 0573597;JP 508149/92;KR 142171;US 5,162,760;US 5,218,329;US 5,250,916;US 5,327,109;US 5,488,335;CA 2114029;FR 9306297;GB 2273393;JP 3205337;KR 115113;CN 93106228.4;US 5,512,866;EP 0706719;DE 0706719;FR 0706719;GB 0706719;CN 95190359.4;US 5,602,518;US 5,721,520;US 5,745,018;EP 0910875;DE 0910875;DK 0910875;FR 0910875;GB 0910875;IE 0910875;JP 505182/98;KR 10-323013;US 5,994,978;US 6,462,629;CN 00810420.4;US 6,559,735;US 6,650,202;US 6,834,429. Other US and foreign patents pending.

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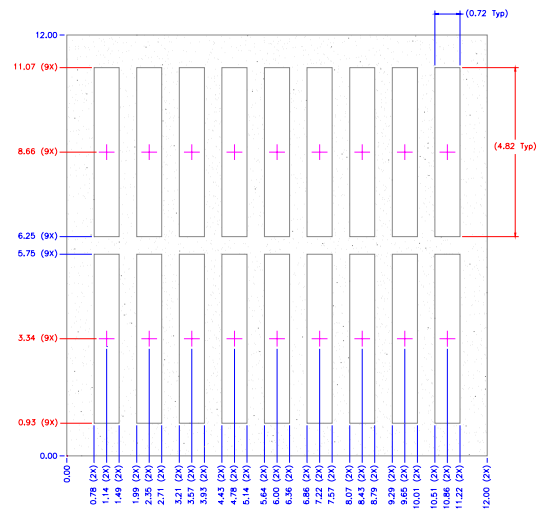
### Mechanical Drawing

Revision A – Origin Date: July 13, 2011 – Revision Date: July 13, 2011

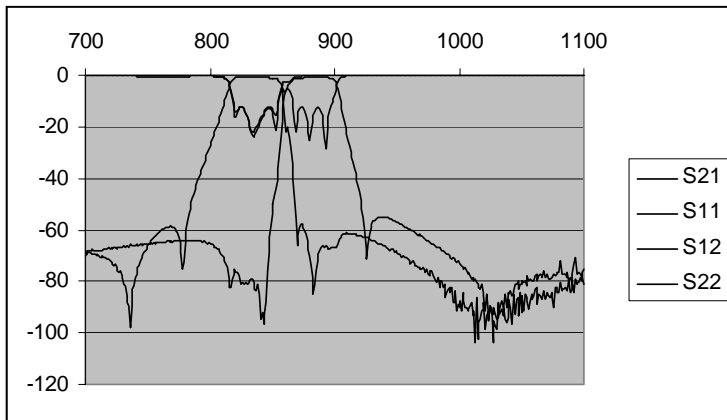


Dim	Nominal (mm)	Tolerance (mm) +/- or max
A	132.0	Max
B	24.5	Max
C	19.9	Max
D	27.0	Max

### Packaging and Marking



### Electrical response



### PCB Layout

For additional detail and the latest drawing please contact CTS

- Filter Outline
- Exposed Conductor
- Solder Resist Over Dielectric
- Solder Resist Over Conductor
- Plated Thru Hole

