

Features

- High Power
- Low Insertion Loss
- High Attenuation

Description

Surface mount, silver (Ag) coated ceramic duplexer for use in WCDMA applications.

Weight: 68.8 grams typical

Material: Filter is composed of a ceramic block plated with Ag and a Bracket made of nickel silver plated steel.

Filter complies with RoHS standards.



Electrical Specifications

Parameter	Frequency MHz	Typical @ 25°C	Spec over -40°C to +85°C
Low Band Response			
Passband lloss	1920 - 1980	-1.00	-1.60
Passband Ripple	1920 - 1980	0.70	1.20
Passband Return Loss @ Low Band	1920 - 1980	-13.50	-11.00
Passband Return Loss @ Ant	1920 - 1980	-13.50	-11.00
Attenuation	2110 - 2170	-67.00	-60.00
High Band Response			
Passband lloss	2110 - 2170	-1.00	-1.60
Passband Ripple	2110 - 2170	0.70	1.20
Passband Return Loss @ High Band	2110 - 2170	-13.50	-11.00
Passband Return Loss @ Ant	2110 - 2170	-13.50	-11.00
Attenuation	1920 - 1980	-67.00	-60.00
Isolation			
Rejection @ Low Band	1920 - 1980	-67.00	-60.00
Rejection @ High Band	2110 - 2170	-67.00	-60.00
Average Power Antenna to High Band port		20 Watt	
Peak Power Antenna to High Band Port		200 Watt	

Note: Supplier shall test each filter to the critical electrical specifications of the above table. Any subsequent audits may deviate from in value due to measurement repeatability among different test systems. Power test will be completed with 50 watts average power in 5 MHz steps across the band. 12 steps total with a 100 millisecond pulse at each frequency point and a 200 watt peak, 1% duty factor with a 9 microsecond pulse. Such deviations shall not exceed the following limits:

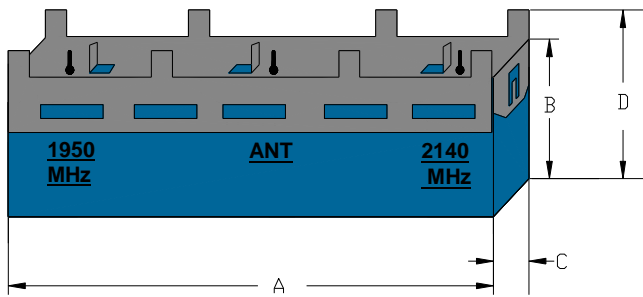
Specification Allowance	
Insertion Loss	0.1 dB
Return Loss	1.0 dB
Stop bands	1.0 d

*This product is covered by one or more of the following U.S. and foreign patents including: US 4,692,726;US 4,742,562; US 4,800,348;US 4,829,274;US 5,146,193;EP 0573597;DE 0573597;FR 0573597;JP 508149/92;KR 142171;US 5,162,760;US 5,218,329;US 5,250,916;US 5,327,109;US 5,488,335;CA 2114029;FR 9306297;GB 2273393;JP 3205337;KR 115113;CN 93106228.4;US 5,512,866;EP 0706719;DE 0706719;FR 0706719;GB 0706719;CN 95190359.4;US 5,602,518;US 5,721,520;US 5,745,018;EP 0910875;DE 0910875;DK 0910875;FR 0910875;GB 0910875;IE 0910875;JP 505182/98;KR 10-323013;US 5,994,978;US 6,462,629;CN 00810420.4;US 6,559,735;US 6,650,202;US 6,834,429. Other US and foreign patents pending.

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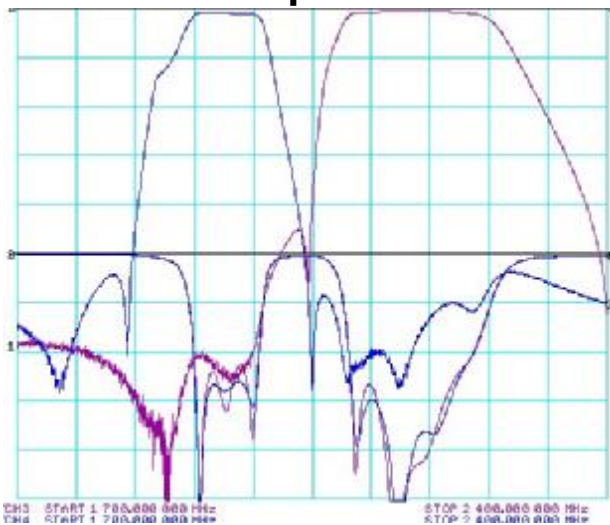
Mechanical Drawing

Revision B – Origin Date: October 21, 2008 – Revision Date: July 28, 2011

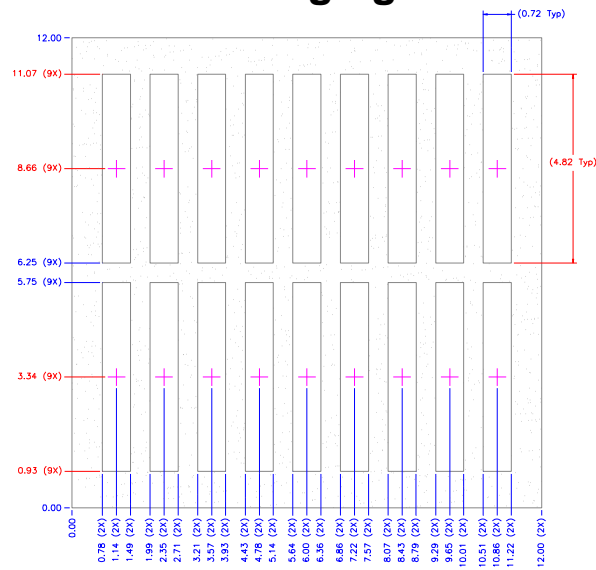


Dim	Nominal (mm)	Tolerance (mm) +/- or max
A	133.1	0.5
B	9.6	max
C	19.9	0.25
D	12.2	max

Electrical response



Packaging



PCB Layout

For additional detail and the latest drawing please contact CTS

- Filter Outline
- ▨ Exposed Conductor
- ▨ Solder Resist Over Dielectric
- ▨ Solder Resist Over Conductor
- Plated Thru Hole

