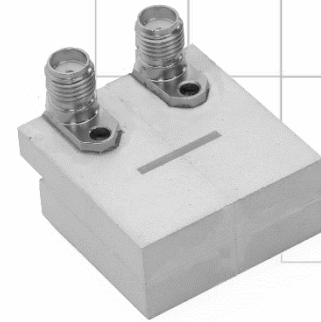


CMB260B - PRELIMINARY 2575-2635 MHz Bandpass Filter

Features

- Low loss with high rejection and low ripple
- Superior power handling and reliability
- Part of the CMB family of Metro-cell TDD Bandpass Filters
- Choice for either PCB mounting with pins, or with various connectors (SMA, SMP-Max, etc.) and mounting brackets



Part Dimensions: 45 x 40 x 20 mm • 80g

Materials: Ag plated ceramic block

Applications

- Wireless Infrastructure applications
- High-performance carrier-grade TDD basestations up to 20W at the antenna port
- Wide-band DAS, Repeaters, or small-cells including support for WiFi co-location

Description

Ceramic waveguide bandpass filter based on ClearPlex technology supports TDD frequency bands. Provides superior rejection, insertion loss, reliability, as well as both peak and average power handling compared to other ceramic technologies. Performance is comparable to compact Air Cavity in dramatically smaller size.

Electrical Specifications

Parameter	Frequency (MHz)	Typical at 25°C	Spec. at 25°C	Spec. over -40°C to +85°C
Nominal Impedance	-	50 ohms	-	-
Average Input Power	-	-	-	32 Watt max
Peak Input Power	-	-	-	320 Watt max

Input-Output Response

Passband Insertion Loss (20 MHz avg)	2575 - 2635	<0.7 dB		0.9 dB max
Passband Ripple	2575 - 2635			0.5 dB max
Group Delay Variation (min-max)	2575 - 2635			20 ns max
Passband Return Loss	2575 - 2635			16 dB min
Attenuation:	1 - 2300			75 dB min
	2300 - 2400			72 dB min
	2400 - 2483			65 dB min
	2483 - 2500			55 dB min
	2700 - 2900			60 dB min
	2700 - 3400			30 dB min

NOTE: Use with RLF0270A lowpass filter to assure high-frequency suppression over 3.4-12.75GHz

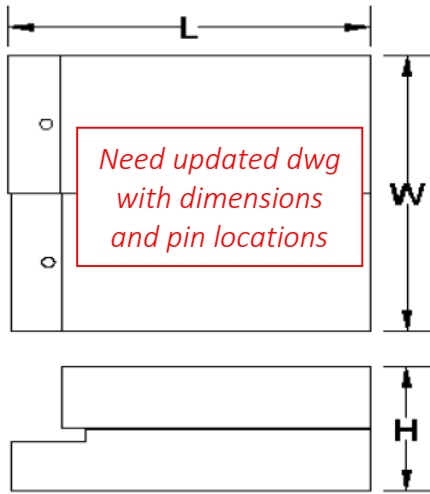
Note: CTS tests each unit to the critical specifications above. Subsequent audits may deviate due to repeatability among different test systems which shall not exceed these allowances.

Specification Allowance	
Insertion Loss	0.1 dB
Return Loss	1.0 dB
Attenuation	1.0 dB

Mechanical Drawing

Size: 45 mm (L) x 40 mm (W) x 20 mm (H)

Weight: 80-90g



PCB Layout

Packaging and Marking

Electrical Response

